AMENDMENTS TO THE SPECIFICATION

Replace the paragraphs on page 1, lines 3-8, with the following:

"Generic Finger Architecture for Spread Spectrum Applications", filed concurrently herewith, having U.S. Serial No. 09/920,094, and now U.S. Patent No. 6,459,883;

"Apparatus and Methods for Sample Selection and Reuse of Rake Fingers in Spread Spectrum Systems", filed concurrently herewith, and having U.S. Serial No. 09/920,095; and

"Apparatus and Method for Configurable Multi-dwell Search Engine for Spread Spectrum Applications", filed concurrently herewith, having U.S. Serial No. 09/919,700, and now U.S. Patent No. 7,003,015.

Replace the paragraph beginning on page 7, line 7, with the following:

In one embodiment, the multi-threaded micro-architecture 100 is a hardware computation resource that can be applied to a single computation process (e.g., a multipath of a given channel). In another embodiment, the computation resource provided by the multi-threaded micro-architecture 100 can be enhanced by running the multi-threaded micro-architecture 100 at a clock rate higher than that required by a process (e.g., higher than the data rate for a communication protocol). In this manner, resources of individual computation components, such as the multi-threaded microarchitecture 100, can be time-shared across multiple computation processes (e.g., several multipaths and/or multiple channels). Additional information on the design

and implementation of configurations into a configurable communication device is provided in a co-pending application bearing serial number 09/492,634, now abandoned, and entitled "Improved Apparatus and Method for Multi-Threaded Signal Processing." This application is commonly assigned and is hereby incorporated for all purposes.

Replace the paragraph beginning on page 8, line 9, with the following:

In an exemplary embodiment, fundamental processing units are defined by applying a profiling process. The fundamental processing units are parameterizable processing blocks that may be application specific but can be enabled for a variety of protocols. The profiling process is performed from a system and hardware perspective to optimize time sliced and multi-threaded architecture. Illustrative examples of fundamental processing units are the hardware kernels described in FIG. 2 of co-pending U.S. application Ser. No. 09/772,584 entitled "A Wireless Spread Spectrum Communication Platform Using Dynamically Reconfigurable Logic." Additional information on the profiling process is provided in co-pending U.S. application Ser. No. 09/565,654, now U.S. Patent No. 6,807,155, and entitled "Method of Profiling Disparate Communications and Signal Processing Standards and Services." These applications are commonly assigned and are hereby incorporated by reference for all purposes.